

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Vishnu K. Agarwal et al.

Title: TRANSISTOR STRUCTURE HAVING REDUCED TRANSISTOR LEAKAGE ATTRIBUTES

Docket No.: 303.780US! Filed: January 17, 2002 Examiner: Hoai V Pham Customer No.: 21186 Serial No.: 10/053,300 Due Date: June 17, 2006 Group Art Unit: 2814 Confirmation No.: 5007

Commissioner for Patents

Attn: MAIL STOP ISSUE FEE

P.O. Box 1450

Alexandria, VA 22313-1450

Notice of Allowance Date:

March 17, 2006

We are transmitting herewith the attached:

X A check in the amount of \$1400.00 to cover the Large Entity Issue Fee Payment.

 \overline{X} A check in the amount of \$300.00 to cover the Publication Fee Payment.

 \overline{X} A check in the amount of \$9.00 to cover the Extra Patent Copies Fec (3 copies).

X Issue Fee Transmittal (Form PTOL-85).

X Communication Re: Fee Address (1 page).

X Amendment Under 37 C.F.R. 1.312 (13 pgs.)

 \underline{X} A return postcard.

Please charge any additional required fees for the Issue Fee Payment or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

Customer Number: 21186

Daniel J. Mertes Reg. No. 55,842

DJM:cmb

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, Attn – MAIL STOP ISSUE FEE, P.O. Box 1450, Alexandria, VA 22313-1450, on this day of June, 2006.

ŲИV

Signature